

WHAT IS CLAIMED IS:

1. A microcomputer comprising: a first electric power source to which a condenser used for backup is added; a second electric power source to which a condenser used for backup is not added; and a resistance load added to the second electric power source,

wherein when a supply of power voltage of the second electric power source is stopped, a level of the second electric power source is changed from a first state to a second state, and when the supply of power voltage of the second electric power source is resumed, the level of the second electric power source is returned from the second state to the first state.

2. A microcomputer according to claim 1, further comprising a reset signal generating circuit for outputting a reset signal according to the change in the level of the second electric power source,

wherein when the level of the second electric power source is returned from the second state to the first state, the reset signal generating circuit outputs a reset signal.

3. A microcomputer according to claim 2, further comprising a comparator circuit for comparing the level of the second electric power source with a predetermined reference voltage,

wherein the reset signal generating circuit outputs a reset

signal according to an output of the comparator circuit.

4. A microcomputer according to claim 3, wherein the load resistance is externally attached to the microcomputer.

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5. A microcomputer according to claim 4, wherein a power voltage is supplied to the comparator circuit from the first electric power source.

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6. A microcomputer according to claim 3, wherein the load resistance is housed in the microcomputer.

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7. A microcomputer according to claim 6, wherein the comparator circuit is used both as a comparator and a load resistance by supplying a power voltage to the comparator circuit from the second electric power source.

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8. A microcomputer comprising:

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a first electric power source to which a condenser used for backup is added; a second electric power source to which a condenser used for backup is not added; an output buffer circuit to which power is supplied from the second electric power source; circuit blocks except for the output buffer circuit, the circuit blocks being supplied with power from the first electric power source; and a resistance load added to the second electric power source,

wherein power voltage of the first and the second electric power source is supplied from an external electric power source circuit, when a supply of power voltage from the electric power source circuit is stopped, a level of the second electric power source is changed from a first state to a second state by the load resistance, and when the supply of power voltage from the electric power source circuit is resumed, the level of the second electric power source is returned from the second state to the first state.

9. A microcomputer according to claim 8,  
further comprising a reset signal generating circuit for outputting a reset signal according to the change in the level of the second electric power source,

wherein when the level of the second electric power source is returned from the second state to the first state, the reset signal generating circuit outputs a reset signal.

10. A microcomputer according to claim 9,  
further comprising a comparator circuit for comparing the level of the second electric power source with a predetermined reference voltage,

wherein the reset signal generating circuit outputs a reset signal according to an output of the comparator circuit.